

[illegible]

PROCESSING MOBILE TRANSCEIVER

PCS-4500 SCHEMATIC DIAGRAM 02

	K2	K3	M MOD
Hx	0	0	A
	0	5	B
	5	D	A-B
	5	5	A-B

	R4	R1	R2	R3
K3	RJLE UP	1000 UP	KHZ 3P	B
K2	MHZ DN	1000 DN	KHZ DN	
K1	M SCAN	M ADDR5	M1A CALL	\$
K4	P SCAN	M CALL	M1B CALL	M

	X1	X2	X3	STEP
R5	0	0	0	5K
	5	0	0	10K
	0	5	0	12.5K (EU 20)
	5	5	0	15K
	0	0	5	20K
	5	0	5	25K
	0	5	5	40K
	5	5	5	50K

O: OPEN
S: SHORT

AND

